

INFORMATION DISCLOSURE STATEMENT

Applicant : Li, et al.
App. No. : 10/614,622
Filed : July 7, 2003
For : METHODS OF INCREASING WRITE
SELECTIVITY IN AN MRAM
Examiner : H. Jey Tsai
Group Art Unit : 2812

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing four (4) references. Copies of disclosed U.S. patents and/or publications are not included pursuant to PTO waiver of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for applications filed after June 30, 2003. Copies of other references, if listed, are enclosed.

This Information Disclosure Statement is being filed with an RCE, and no fee is required in accordance with 37 C.F.R. § 1.97 (b)(4). Identification herein is not an admission that any of the foregoing references are prior art to the above-captioned application.

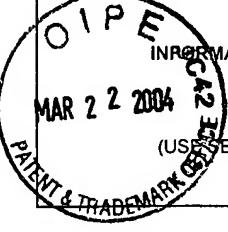
Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: March 19, 2004

By: *Michael Okamoto*

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| FORM PTO-1449  INFORMATION DISCLOSURE STATEMENT BY APPLICANT MAR 22 2004 (USE SEVERAL SHEETS IF NECESSARY) | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE ATTY. DOCKET NO. MICRON.198C1DV1 | APPLICATION NO. 10/614,622 |
| | APPLICANT Li, et al. | |
| | FILING DATE July 7, 2003 | GROUP 2812 |

U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPROPRIATE) |
|------------------|-----------------|----------|-----------------------|-------|----------|------------------------------|
| | 6,104,633 | 8/15/00 | Abraham, et al. | | | Feb. 10, 1998 |
| | 6,134,139 | 10/17/00 | Bhattacharyya, et al. | | | Jul. 28, 1999 |
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FOREIGN PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | |
|------------------|-----------------|------|---------|-------|----------|-------------|----|
| | | | | | | YES | NO |
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| EXAMINER INITIAL | OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) |
|------------------|---|
| | M. Durlam, et al. "A low power 1Mbit MRAM based on 1T1MTJ bit cell integrated with Copper Interconnects," 2002 <i>Symposium on VLSI Circuits, Digest of Technical Papers</i> (IEEE June 2002), pp. 158-161. |
| | M. Durlam, et al., "A 0.18µm 4Mb Toggling MRAM," 2003 <i>IEEE International Electron Devices Meeting, IEDM 2003 Technical Digest</i> (IEEE 2003), pp. 995-997. |
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| EXAMINER | DATE CONSIDERED |
| *EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT. | |